Class	Subclass	ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED 1004 Q.A. SW	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/841536		712	34	2183	Eddie Pan

APPLICANTS
Sanjay Agarwal
Sapna Agrawal

TITLE OF INVENTION:
Coprocessor architecture for control processors using synchronous logic

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)	
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)
INTERNATIONAL CLASSIFICATION			

☐ Continued on Issue Slip Inside File Jacket

☐ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
	14			3	

☐ The term of this patent subsequent to _____ (date) has been disclaimed.

_____ (Assistant Examiner) _____ (Date)

NOTICE OF ALLOWANCE MAILED

☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____

_____ (Primary Examiner) _____ (Date)

ISSUE FEE

Amount Due	Date Paid

☐ The terminal ____months of this patent have been disclaimed.

_____ (Legal Instruments Examiner) _____ (Date)

ISSUE BATCH NUMBER

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